Surface Mount General Purpose Silicon EPI Diode

Reverse Voltage - 1000 V

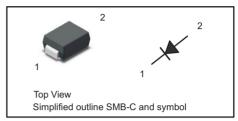
Forward Current - 2 A

FEATURES

- For surface mounted applications
- · Low profile package
- · Glass Passivated Chip Junction
- Easy to pick and place
- Lead free in comply with EU RoHS 2011/65/EU directives

PINNING

PIN	DESCRIPTION	
1	Cathode	
2	Anode	



MECHANICAL DATA

· Case: SMB-C

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.1g / 0.003oz

Maximum Ratings and Electrical characteristics

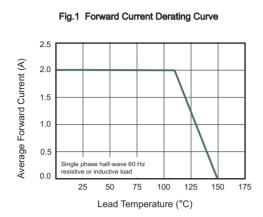
Ratings at 25 °C ambient temperature unless otherwise specified.

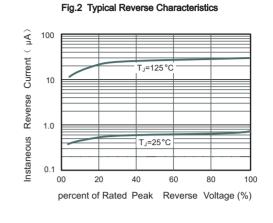
Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

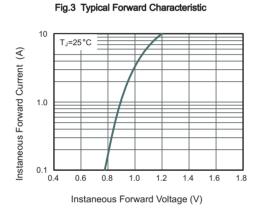
Parameter	Symbols	SMUR2MBC	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	1000	٧
Maximum RMS voltage	V _{RMS}	700	V
Maximum DC Blocking Voltage	V _{DC}	1000	V
Maximum Average Forward Rectified Current @ Fig.1	I _{F(AV)}	2	А
Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	60	А
Peak Forward Surge Current,1.0ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	120	А
I ² t Rating for fusing (3ms≤t≤8.3ms)	l ² t	14.9	A ² S
Max Instantaneous Forward Voltage at 2 A	V _F	1.1	V
Maximum DC Reverse Current $T_a = 25$ °C at Rated DC Reverse Voltage $T_a = 125$ °C	I _R	5 100	μA
Typical Junction Capacitance (1)	C _j	20	pF
Typical Thermal Resistance (2)	R _{0JA} R _{0JC} R _{0JL}	43 9 18	°C/W
Operating and Storage Temperature Range	T_j, T_{stg}	-55 ~ + 150	°C

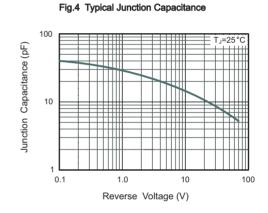
⁽¹⁾ Measured at 1 MHz and applied reverse voltage of 4 V D.C

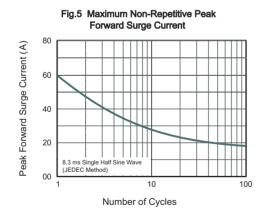
⁽²⁾ P.C.B. mounted with 1.5" X 1.5" (3.81 X 3.81 cm) copper pad areas.







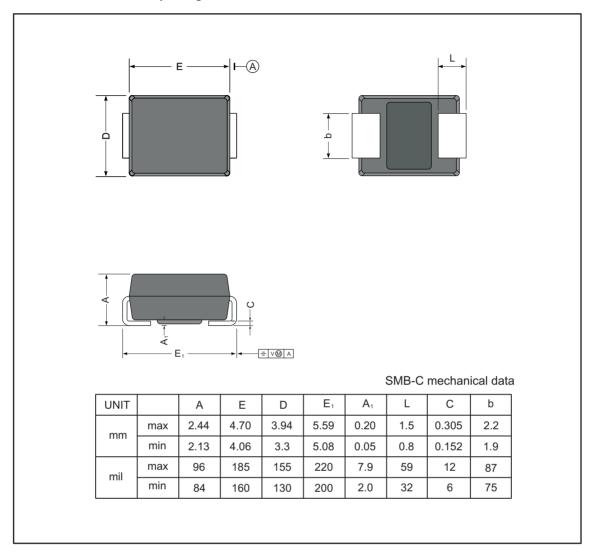




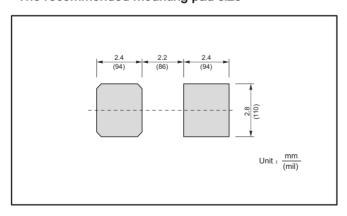
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SMB-C



The recommended mounting pad size



Marking

Type number	Marking code
SMUR2MBC	SM2M

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